USB-1608G Series
16-Bit High-Speed Multifunction DAQ Devices

USB-1608G Series provides eight differential or 16 single-ended analog inputs, two analog outputs (USB-1608GX-2AO), 8 digital I/O, and one counter input.

Overview
USB-1608G Series devices are low-cost, high-speed, analog and digital I/O USB devices. All of these devices offer up to eight DIFF or 16 SE analog inputs, eight digital I/O channels, two counter inputs, and one timer output.

The USB-1608GX-2AO standard and OEM version offers two, 16-bit analog output channels with DAC rates up to 500 kS/s.

Analog Input
USB-1608G Series devices provide 16-bit analog inputs that are software-selectable as eight DIFF or 16 SE inputs. These devices also support input ranges of ±10 V, ±5 V, ±2 V, and ±1 V that are software-selectable per channel.

Analog Output
(USB-1608GX-2AO only)
The USB-1608GX-2AO standard/OEM board has two 16-bit analog outputs that can be updated at a rate of 250 kS/s per channel; one output can be updated at a rate of 500 kS/s. The output range is fixed at ±10 V.

Trigger Input
USB-1608G Series devices have an external digital trigger input. The trigger mode is software-selectable for edge- or level-sensitive mode. You can configure edge-sensitive mode for either rising or falling edge. In level-sensitive mode, you can configure for either high or low level. The default setting at power up is edge-sensitive, rising edge.

Analog Output
(USB-1608GX-2AO only)
The USB-1608GX-2AO standard/OEM board has two 16-bit analog outputs that can be updated at a rate of 250 kS/s per channel; one output can be updated at a rate of 500 kS/s. The output range is fixed at ±10 V.

Retrigger Mode
Users can set up repetitive analog input trigger events. The trigger is automatically re-armed after it is activated. The A/D trigger count (the number of samples you want per trigger) is configurable with software.

Digital I/O
Eight bidirectional digital I/O lines are individually configurable for input or output. The DIO terminals can detect the state of any TTL-level input. Users can configure for pull-up (+5 V) or pull-down (0 V) with an onboard jumper.

Counter Input
Two 32-bit event counters are provided to count TTL pulses. The counters accept inputs of up to 20 MHz.

Timer Output
The PWM timer output generates a pulse output with a programmable frequency in the range of 0.0149 Hz to 32 MHz. The timer output parameters are software-selectable.

Features
- 16-bit high-speed USB devices
- Acquisition rates up to 500 kS/s
- 8 differential (DIFF) or 16 single-ended (SE) analog inputs
- Up to 2 analog outputs
- 8 digital I/O
- Two 32-bit counter inputs
- One timer output
- OEM version available

Supported Operating Systems
- Windows® 11/10/8/7/Vista®/XP 32/64-bit
- Linux®
- Android™

USB-1608G Series Selection Chart

<table>
<thead>
<tr>
<th>Model</th>
<th>Analog Inputs</th>
<th>Sample Rate (max)</th>
<th>Analog Outputs</th>
<th>Signal I/O</th>
<th>USB Cable and SW CD</th>
</tr>
</thead>
<tbody>
<tr>
<td>USB-1608G</td>
<td>16 SE/8 DIFF</td>
<td>250 kS/s</td>
<td>0</td>
<td>Screw Terminal</td>
<td>✔</td>
</tr>
<tr>
<td>USB-1608GX</td>
<td>16 SE/8 DIFF</td>
<td>500 kS/s</td>
<td>0</td>
<td>Screw Terminal</td>
<td>✔</td>
</tr>
<tr>
<td>USB-1608GX-2AO</td>
<td>16 SE/8 DIFF</td>
<td>500 kS/s</td>
<td>2</td>
<td>Screw Terminal</td>
<td>✔</td>
</tr>
<tr>
<td>USB-1608G-OEM</td>
<td>16 SE/8 DIFF</td>
<td>250 kS/s</td>
<td>0</td>
<td>Header</td>
<td>–</td>
</tr>
<tr>
<td>USB-1608GX-OEM</td>
<td>16 SE/8 DIFF</td>
<td>500 kS/s</td>
<td>0</td>
<td>Header</td>
<td>–</td>
</tr>
<tr>
<td>USB-1608GX-2AO-OEM</td>
<td>16 SE/8 DIFF</td>
<td>500 kS/s</td>
<td>2</td>
<td>Header</td>
<td>–</td>
</tr>
</tbody>
</table>

Measurement Computing
(508) 946-5100
info@mccdaq.com
mccdaq.com
USB-1608G Series

Overview

Clock Output
The AI clock pin outputs the signal from the internal clock.

Calibration
The USB-1608G Series is factory-calibrated using a NIST-traceable calibration process. Specifications are guaranteed for one year. The USB-1608G Series also supports field calibration for users to calibrate the device locally with the InstaCal utility.

USB-1608G Series OEM Versions
OEM versions have board-only form factors with header connectors for OEM and embedded applications (no case, CD, or USB cable included). All devices can be further customized to meet customer needs.

The OEM versions have the same specifications as the standard devices, but come in a board-only form factor with header connectors instead of screw terminals.
## USB-1608G Series

### Software

#### Software Support

USB-1608G Series devices are supported by the software in the table below.

<table>
<thead>
<tr>
<th>Ready-to-Run Applications</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>DAQami™</strong></td>
</tr>
<tr>
<td><strong>InstaCal™</strong></td>
</tr>
<tr>
<td><strong>TracerDAQ™ and TracerDAQ Pro</strong></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>General-Purpose Programming Support</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>UL for Linux®</strong></td>
</tr>
<tr>
<td><strong>UL for Android™</strong></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Application-Specific Programming Support</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>ULx for NI LabVIEW™</strong></td>
</tr>
<tr>
<td><strong>DASYLab®</strong></td>
</tr>
<tr>
<td><strong>MATLAB® driver</strong></td>
</tr>
</tbody>
</table>
USB-1608G Series

Specifications

These specifications apply to both standard and OEM versions unless noted.

Analog Input

A/D converter type: Successive approximation
ADC resolution: 16 bits
Number of channels: 8 DIFF, 16 SE; software-selectable
Input voltage range: ±10 V, ±5 V, ±2 V, ±1 V; software-selectable per channel
Absolute maximum input voltage
CHx relative to AGND: ±25 V max (power on); ±15 V max (power off)
Input Impedance: 1 GΩ (power on); 820 Ω (power off)
Input bias current: ±10 nA (power off); 820 nA (power on); 1 GΩ
Input Impedance: 1 GΩ
Input bandwidth: all input ranges, small signal (–3 dB) ±10.2 V max relative to AGND
±5 V range: ±9.5 V max relative to AGND ±2 V range: ±9.0 V max relative to AGND ±1 V range: ±8.5 V max relative to AGND
Maximum working voltage (signal + common mode) ±10 V range: ±10.2 V max relative to AGND ±5 V range: ±10.2 V max relative to AGND ±2 V range: ±9.5 V max relative to AGND ±1 V range: ±9.0 V max relative to AGND

Analog Input DC Voltage Measurement

<table>
<thead>
<tr>
<th>Range</th>
<th>Gain Error (% of Reading)</th>
<th>Offset Error (µV)</th>
<th>INL Error (% of Range)</th>
<th>Absolute Accuracy at Full Scale (µV)</th>
<th>Gain Temperature Coefficient (% reading/°C)</th>
<th>Offset Temperature Coefficient (µV/°C)</th>
</tr>
</thead>
<tbody>
<tr>
<td>±10 V</td>
<td>0.024</td>
<td>915</td>
<td>0.0076</td>
<td>4075</td>
<td>0.0014</td>
<td>47</td>
</tr>
<tr>
<td>±5 V</td>
<td>0.024</td>
<td>686</td>
<td>0.0076</td>
<td>2266</td>
<td>0.0014</td>
<td>24</td>
</tr>
<tr>
<td>±2 V</td>
<td>0.024</td>
<td>336</td>
<td>0.0076</td>
<td>968</td>
<td>0.0014</td>
<td>10</td>
</tr>
<tr>
<td>±1 V</td>
<td>0.024</td>
<td>245</td>
<td>0.0076</td>
<td>561</td>
<td>0.0014</td>
<td>5</td>
</tr>
</tbody>
</table>

Noise Performance*

<table>
<thead>
<tr>
<th>Range</th>
<th>Counts</th>
<th>LSBrms</th>
</tr>
</thead>
<tbody>
<tr>
<td>±10 V</td>
<td>6</td>
<td>0.91</td>
</tr>
<tr>
<td>±5 V</td>
<td>6</td>
<td>0.91</td>
</tr>
<tr>
<td>±2 V</td>
<td>7</td>
<td>1.06</td>
</tr>
<tr>
<td>±1 V</td>
<td>9</td>
<td>1.36</td>
</tr>
</tbody>
</table>

** For the peak-to-peak noise distribution test, a differential input channel is connected to AGND at the input terminal block, and 32,000 samples are acquired at the maximum rate available at each setting.

Settling Time**

USB-1608G

<table>
<thead>
<tr>
<th>Range</th>
<th>4 µS settling accuracy (% FSR)</th>
<th>6 µS settling accuracy (% FSR)</th>
<th>10 µS settling accuracy (% FSR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>±10 V</td>
<td>0.0061</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±5 V</td>
<td>0.0061</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±2 V</td>
<td>0.0061</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±1 V</td>
<td>0.0061</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
</tbody>
</table>

USB-1608GX/1608GX-2AO

<table>
<thead>
<tr>
<th>Range</th>
<th>2 µS settling accuracy (% FSR)</th>
<th>4 µS settling accuracy (% FSR)</th>
<th>9 µS settling accuracy (% FSR)</th>
</tr>
</thead>
<tbody>
<tr>
<td>±10 V</td>
<td>0.1251</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±5 V</td>
<td>0.0687</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±2 V</td>
<td>0.0687</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
<tr>
<td>±1 V</td>
<td>0.0687</td>
<td>0.0031</td>
<td>0.0015</td>
</tr>
</tbody>
</table>

Common mode rejection ratio
fref = 60 Hz, all input ranges: 86 dB
Crosstalk
Adjacent differential mode channels, DC to 100 kHz: –75 dB
Input coupling: DC
Sampling rate (software-selectable)
USB-1608G: 0.0149 Hz to 250 kHz
USB-1608GX/1608GX-2AO: 0.0149 Hz to 500 kHz
Trigger source: TRIG (refer to “External Trigger” on page 5)
Sample clock source: Internal A/D clock or external A/D clock (AICKI terminal)
Burst mode: Software-selectable using the internal A/D clock; always enabled when using the external clock (AICKI terminal)
USB-1608G: 4 μs
USB-1608GX/1608GX-2AO: 2 μs
Throughput
Software paced: 33 to 4000 S/s typ, system dependent
Hardware paced
USB-1608G: 250 kS/s max
USB-1608GX/1608GX-2AO: 500 kS/s max
Channel gain queue: Up to 16 elements; software-selectable range for each channel
Warm-up time: 15 minutes min

* For the peak-to-peak noise distribution test, a differential input channel is connected to AGND at the input terminal block, and 32,000 samples are acquired at the maximum rate available at each setting.

** Settling time is defined as the expected accuracy after one conversion when switching from a channel with a DC input at one extreme of full scale to another channel with a DC input at the other extreme of full scale. Both input channels are configured for the same input range.

Analog Output (USB-1608GX-2AO only)

Number of channels: 2 (leave unused AOUTx output channels disconnected)
Resolution: 16 bits
Output range: ±10 V (calibrated)
Output transient
Host computer is reset, powered on, suspended, or a reset command is issued to the device
Duration: 500 µs
Amplitude: 2 V pk-pk
Powered off
Duration: 10 ms
Amplitude: 7 V peak
Differential non-linearity: ±0.25 LSB typ; ±1 LSB max
Output current
AOUTx: ±3.5 mA max
Output short-circuit protection
AOUTx connected to AGND: Unlimited duration
Output coupling: DC
Power on and reset state
DACs cleared to zero-scale: 0 V, ±50 mV (AOUTx defaults to 0 V when the host is reset, powered on, suspended, or a reset command is issued to the device)
Output noise: 30 µVrms
Trigger source: TRIG (refer to “External Trigger” on page 5)
Sample clock source: Internal D/A clock or external D/A clock (AOCKI terminal)
Output update rate: 500 kHz/number of channels in the scan
Settling Time
To rated accuracy, 10 V step: 40 μs
Throughput
Software paced: 33 S/s to 4000 S/s typ, system-dependent
Hardware paced: 500 kS/s max, system-dependent
USB-1608G Series

Specifications

Calibrated Absolute Accuracy
Range: ±10 V
Absolute accuracy (± LSB): 16.0

Calibrated Absolute Accuracy Components
Range: ±10 V
% of reading: ±0.0183
Offset (±mV): 1.831
Offset tempco (µV/°C): 12.7
Gain tempco (ppm of range/°C): 13

Analog Output Relative Accuracy
Range: ±10 V
Relative accuracy (INL): 4.0 typ

Analog Input/Output Calibration
Recommended Warm-up Time: 15 minutes min
Calibration method: Self-calibration (firmware)
Calibration interval: 1 year (factory calibration)
AI calibration reference
+5 V, ±2.5 mV max (actual measured values stored in EEPROM)
Tempco: 5 ppm/°C max
Long term stability: 15 ppm/1000 hours
AO/UTx calibration procedure (USB-1608GX-2AO only)
The AOUTx terminals are internally routed to the AI circuit. For best results, disconnect AOUTx connections at the terminal block before calibrating.

Digital I/O
Digital type: CMOS
Number of I/O: 8
Configuration: Bit-configurable as input (power on default) or output
Pull-up configuration: 47 kΩ resistors configurable as pull-up/down (default) via internal jumper (W1).
Digital I/O transfer rate (system-paced): 33 to 8000 port reads/writes or single bit reads/writes per second typ, system dependent.
Input high voltage: 2.0 V min, 5.5 V absolute max
Input low voltage: 0.8 V max, –0.5 V absolute min, 0 V recommended min
Output high voltage: 4.4 V min (IOH = –50 µA), 3.76 V min (IOH = –2.5 mA)
Output low voltage: 0.1 V max (IOL = 50 µA), 0.44 V max (IOL = 2.5 mA)
Output current: ±2.5 mA max

External Trigger
Trigger source: TRIG input
Trigger mode: Software configurable for edge or level sensitive, rising or falling edge, high or low level. Power on default is edge sensitive, rising edge.
Trigger latency: 1 µs + 1 clock cycle max
Trigger pulse width: 100 ns min
Input type: Schmitt trigger, 33 Ω series resistor and 49.9 kΩ pull-down to ground
Schmitt trigger hysteresis: 0.4 V to 1.2 V
Input high voltage: 2.2 V min, 5.5 V absolute max
Input low voltage: 1.5 V max, –0.5 V absolute min, 0 V recommended min

External Clock I/O
Terminal names
AICKI, AICKO
USB-1608GX-2AO: AICK, AICKO AOCKI, AOCKKO
Terminal types
AxCKI: Input, active on rising edge
AxCKO: Output, power on default is 0 V, active on rising edge
Terminal descriptions
AxCKI: Receives sampling clock from external source
AxCKO: Outputs the internal sampling clock (D/A or A/D clock) or the pulse generated from AxCKI when in external clock mode.
Input clock rate
USB-1608G: 250 kHz max
USB-1608GX/1608GX-2AO: 500 kHz max
Clock pulse width
AxCKI: 400 ns min
AxCKO: 400 ns min

Input type: Schmitt trigger, 33 Ω series resistor, 47 kΩ pull-down to ground
Schmitt trigger hysteresis: 0.4 V to 1.2 V
Input high voltage: 2.2 V min, 5.5 V absolute max
Input low voltage: 1.5 V max, –0.5 V absolute min, 0 V recommended min
Output high voltage: 4.4 V min (IOH = –50 µA), 3.76 V min (IOH = –2.5 mA)
Output low voltage: 0.1 V max (IOL = 50 µA), 0.44 V max (IOL = 2.5 mA)
Output current: ±2.5 mA max

Counter Input
Terminal names: CTR0, CTR1
Number of channels: 2 channels
Resolution: 32-bit
Counter type: Event counter
Input type: Schmitt trigger, 33 Ω series resistor, 47 kΩ pull-down to ground
Input source:
CTR0 (terminal S2)
CTR1 (terminal S1)

Counter read/Writes rates (Software-paced): 33 to 8000 reads/writes per second typ, system dependent
Input high voltage: 2.2 V min, 5.5 V max
Input low voltage: 1.5 V max, –0.5 V min
Schmitt trigger hysteresis: 0.4 V min, 1.2 V max
Input frequency: 20 MHz, max
High pulse width: 25 ns, min
Low pulse width: 25 ns, min

Timer Output
Timer terminal name: TMR
Timer type: PWM output with count, period, delay, and pulse width registers
Internal clock frequency: 64 MHz
Register widths: 32-bit
High pulse width: 15.625 ns min
Low pulse width: 15.625 ns min
Output high voltage: 4.4 V min (IOH = –50 µA), 3.76 V min (IOH = –2.5 mA)
Output low voltage: 0.1 V max (IOL = 50 µA), 0.44 V max (IOL = 2.5 mA)
Output Current: ±2.5 mA max

Memory
Data FIFO:
USB-1608G/1608GX: 4 kS analog input
USB-1608GX-2AO: 4 kS analog input, 2 kS analog output
Non-volatile memory: 32 KB (28 KB firmware storage, 4 KB calibration/user data)

Power
Supply current
The total quiescent current requirement includes up to 10 mA for the Status LED; this does not include any potential loading of the digital I/O bits, +5 V terminal, or the AOUTx outputs (USB-1608GX-2AO only).
Quiescent Current
USB-1608G/1608GX: 230 mA
USB-1608GX-2AO: 260 mA
+5 V user output voltage range (available at Terminal 43): 4.5 V min to 5.25 V max
+5 V user output current (available at terminal 43): 10 mA max

Environmental
Operating temperature range: 0 °C to 55 °C max
Storage temperature range: –40 °C to 85 °C max
Humidity: 0% to 90% non-condensing max

Mechanical
Signal I/O connector
–OEM models: Two 28-pin, 0.1 in. pitch headers
All other devices: 2 banks of screw-terminal blocks (wire gauge range 16 AWG to 30 AWG)
Dimensions (L x W x H):
Standard version: 127 × 89.9 × 35.6 mm (5.00 × 3.53 × 1.40 in.)
OEM version: 121.92 × 86.36 × 15.24 (4.80 × 3.40 × 0.60 in.)
## USB-1608G Series

### Ordering

#### Order Information

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Part No.</strong></td>
<td><strong>Description</strong></td>
</tr>
<tr>
<td>USB-1608G</td>
<td>16-channel, 250 kS/s sampling DAQ device with two 32-bit counter inputs, one timer output, and eight digital I/O lines. Includes a USB cable and MCC DAQ software.</td>
</tr>
<tr>
<td>USB-1608GX</td>
<td>16-channel, 500 kS/s sampling DAQ device with two 32-bit counter inputs, one timer output, and eight digital I/O lines. Includes a USB cable and MCC DAQ software.</td>
</tr>
<tr>
<td>USB-1608GX-2AO</td>
<td>16-channel, 500 kS/s sampling DAQ device with two analog outputs, two 32-bit counter inputs, one timer output, and eight digital I/O lines. Includes a USB cable and MCC DAQ software.</td>
</tr>
<tr>
<td>USB-1608G-OEM</td>
<td>Board-only DAQ device with 16-channels, 250 kS/s sampling, two 32-bit counter inputs, one timer output, and eight digital I/O lines.</td>
</tr>
<tr>
<td>USB-1608GX-OEM</td>
<td>Board-only DAQ device with 16-channels, 500 kS/s sampling, two 32-bit counter inputs, one timer output, and eight digital I/O lines.</td>
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</tr>
</tbody>
</table>

#### Software also Available from MCC

- **TracerDAQ Pro**: Out-of-the-box virtual instrument suite with strip chart, oscilloscope, function generator, and rate generator – professional version
- **DASYLab**: Icon-based data acquisition, graphics, control, and analysis software