

CIO-TERM100
&
CIO-TERM100/DST
User's Manual



Revision 1A

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1 INTRODUCTION & PHYSICAL DESCRIPTION

The CIO-TERM100 and its variant CIO-TERM100/DST are 100-pin screw terminal boards intended for use with high-density digital I/O boards. Each has two, 50-pin male IDC connectors that accept our C50FF-# cables. The screw terminals will accept 12 to 22 AWG wire. See Figures 1-1 and 1-2 for board connector vs. terminal strip layouts.

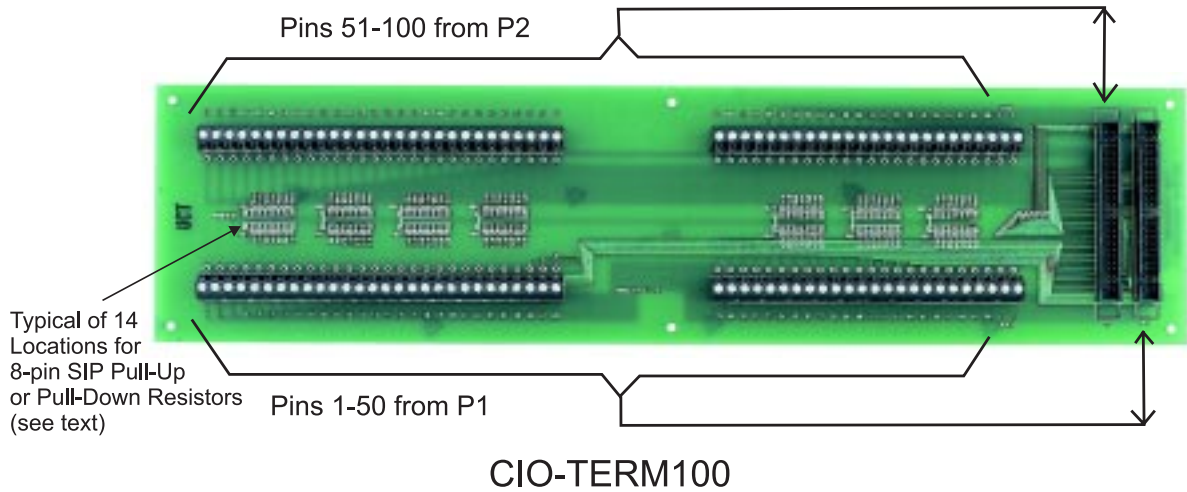


Figure 1-1. CIO-TERM100 Board Layout

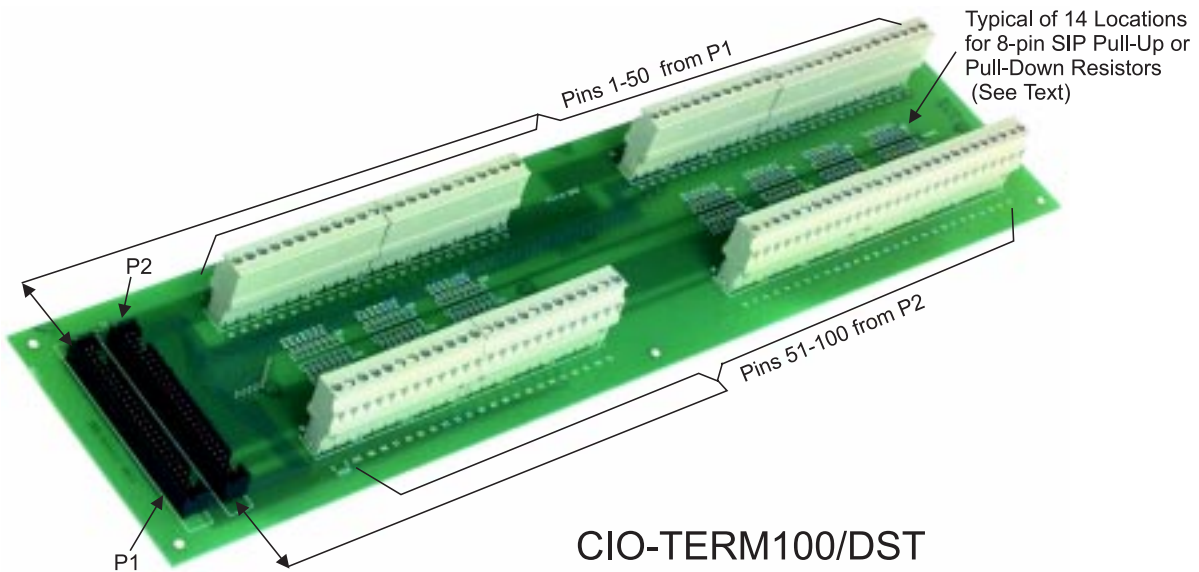


Figure 1-2. CIO-TERM100/DST Board Layout

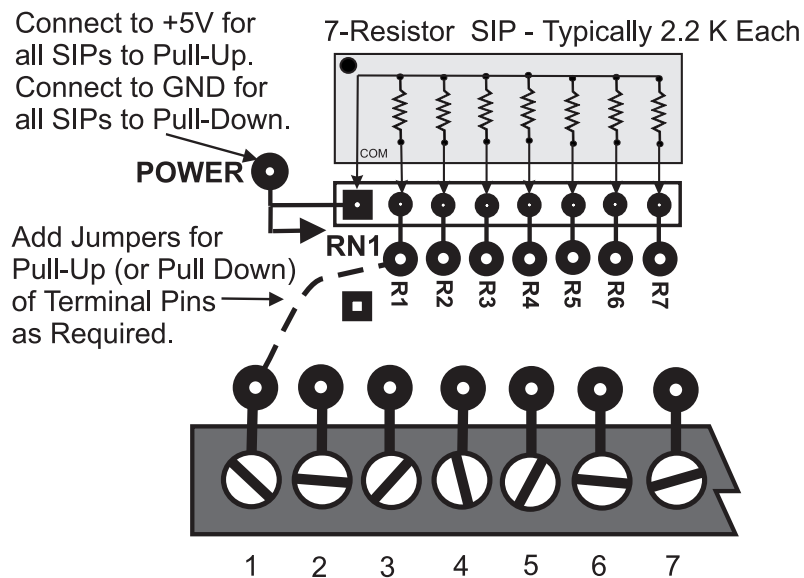
The board has 14 open mounting positions where 8-pin, 7-resistor Single Inline Packages (SIPs) can be installed when pull-up (or pull-down) resistors are necessary or desirable. The common terminals on the SIPs are all common to each other on the board. The board connection point is labeled "POWER".

Thus, if the POWER point on the board is tied to +5VDC, any SIP installed and jumper-wired to screw terminals will have those pins pulled high (Figure 1-3).

Alternatively, if the POWER point is tied to Ground, any (and all) installed SIPs resistors will pull their associated screw terminals to ground.

We recommend using 2.2Kohm resistors for pull-up or pull-down. You can obtain these from us: the part number is 2.2K*7PU.

NOTE: *SIPs are not connected to screw terminals by board etch.* After a SIP is installed, you must install jumpers to each resistor from the screw terminals as required.



SIP INSTALLATION - CIO-TERM100 -100/DST

Figure 1-3. SIP Installation

Most boards that you would use this terminal board with have SIP locations also. Either location can be used with equal efficiency. However, it can be useful to have both. If one or more digital lines do not require termination, there is an advantage to using the resistor locations on the CIO-TERM100 since the resistors are individually jumpered to each digital IO line.

If you are unfamiliar with pull-up/pull-down resistor theory, refer to the manual for the Digital I/O board. See Figure 1-4 and 1-5 for schematics of the CIO-TERM100 and CIO-TERM100/DST terminal boards.

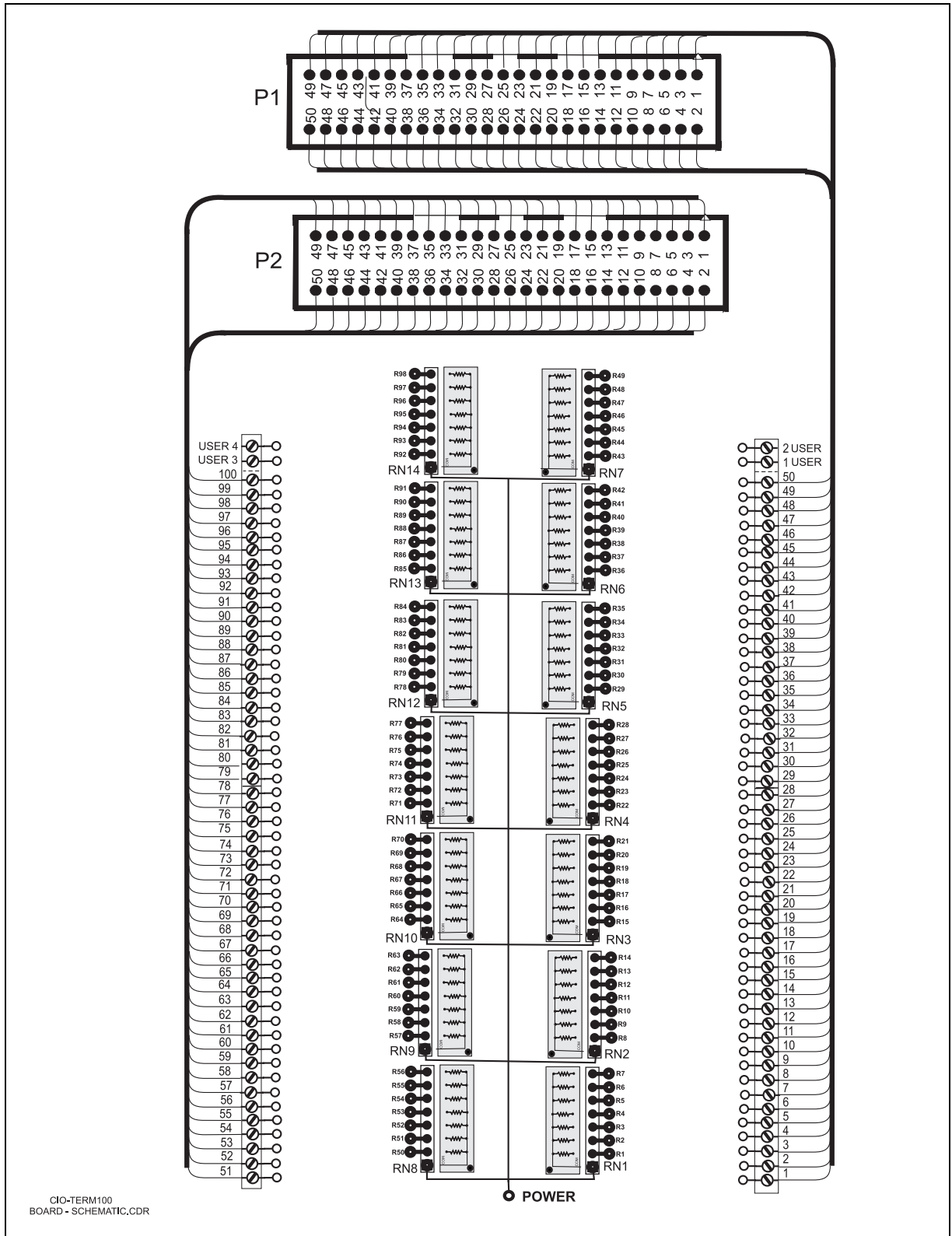


Figure 1-4. CIO-TERM100 Schematic

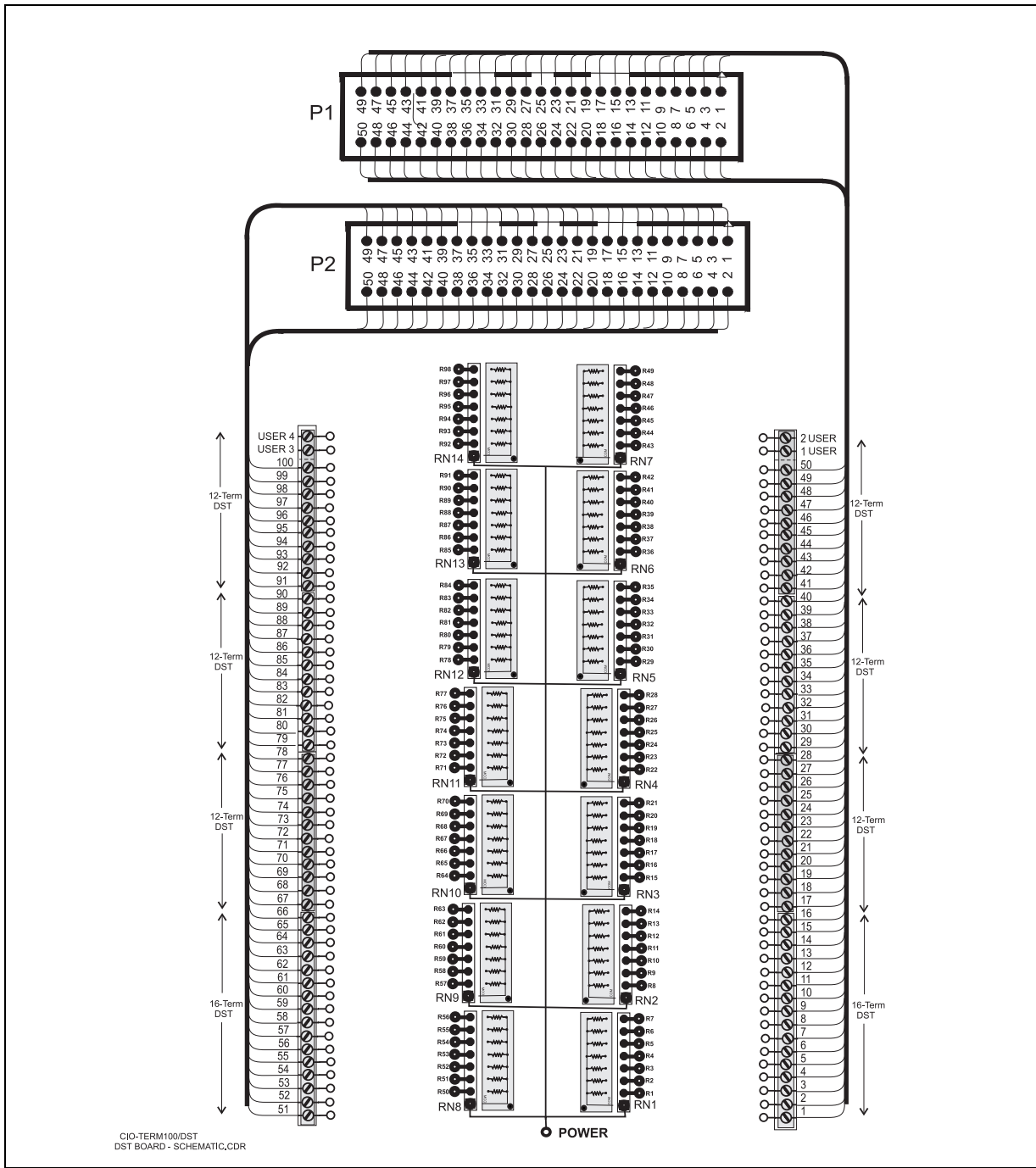


Figure 1-5. CIO-TERM100/DST Schematic

EC Declaration of Conformity

We, Measurement Computing Corp., declare under sole responsibility that the product:

CIO-TERM100	Screw Terminal Board
CIO-TERM100/DST	Screw Terminal Board with Detachable Screw Terminals
<hr/> Part Number	<hr/> Description

to which this declaration relates, meets the essential requirements, is in conformity with, and CE marking has been applied according to the relevant EC Directives listed below using the relevant section of the following EC standards and other normative documents:

EU EMC Directive 89/336/EEC: Essential requirements relating to electromagnetic compatibility.

EU 55022 Class B: Limits and methods of measurements of radio interference characteristics of information technology equipment.

EN 50082-1: EC generic immunity requirements.

IEC 801-2: Electrostatic discharge requirements for industrial process measurement and control equipment.

IEC 801-3: Radiated electromagnetic field requirements for industrial process measurements and control equipment.

IEC 801-4: Electrically fast transients for industrial process measurement and control equipment.

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