

Arty A7-100T™ Statement of Volatility

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 This document applies to the Arty A7 rev. E.X

This document lists the location, purpose, capacity, volatility and (re)programmability of memory devices that may be installed on the Arty A7-100T (SKU: 410-319-1). The terms programmable and erasable refer to normal means of access available to the public. It does not include reverse-engineering or any other attempts to extract data from these devices.

The content of this document is provided for information purposes only.

Volatile Memory

IC	Memory Purpose	User programmable	User removable	Size	Reset procedure
MT41K128M16JT-125 IT:K, MT41K128M16JT-125 IT:K TR, MT41J128M16(JT/TW)-125 ICDDR3SDRAM2GBIT96FBGA, IS43TR16128B-125KBL, or V73CAG02168R(A/B/C)J	DDR3 Program / Data memory	Yes	No	2 Gib	Remove power for 60 seconds
XC7A100T-1CSG324	Configuration data for internal latches	Yes	No	30,606,304 bits	Remove power for 60 seconds
XC7A100T-1CSG324	Block RAM	Yes	No	4,860 Kib	Remove power for 60 seconds
XC7A100T-1CSG324	Distributed RAM	Yes	No	1,188 Kib	Remove power for 60 seconds

Non-Volatile Memory

IC	Memory Purpose	Technology	User programmable	User removable	Size	Reset procedure
93LC56BT-I/OT	USB ID and Configuration	EEPROM	No	No	2 KiB	Available through support forum on request
S25FL128SAGMF[A B M V]00 or S25FL127SABMFV000	FPGA configuration and data storage	FLASH	Yes	No	128 Mib	Electrically erasable; Use iMPACT or Vivado Software Tools available from Xilinx