

## Cmod A7-15T™ Statement of Volatility

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 This document applies to the Cmod A7-15T rev. C.

This document lists the location, purpose, capacity, volatility and (re)programmability of memory devices that may be installed on the Digilent Cmod A7-15T. The terms programmable and erasable refer to normal means of access available to the public. It does not include reverse-engineering or any other attempts to extract data from these devices.

The content of this document is provided for information purposes only.

### Volatile memory

IC	Memory Purpose	User programmable	User removable	Size	Reset procedure
XC7A15T-1CPG236C	Block RAM	Yes	No	900Kib	Remove power for 60 seconds
XC7A15T-1CPG236C	Distributed RAM	Yes	No	200Kib	Remove power for 60 seconds
IS61WV5128BLL-10BLI	Static RAM	Yes	No	512 KByte	Remove power for 60 seconds

## Non-volatile memory

IC	Memory Purpose	Technology	User programmable	User removable	Size	Reset procedure
EEPROM	ID and Configuration	EEPROM	Yes	No	2Kbit	Electrically erasable; Using Digilent's API provides the ability to write three different string fields, with maximum lengths of 15, 16, and 28 bytes. However, there is nothing to prevent a user from overwriting the contents of the entire device.
MX25L3233FZBI-08G/Q	FPGA configuration and data storage	Flash	Yes	No	32Mib	Electrically erasable; Use Vivado Hardware manager or SDK available in the Xilinx Tool
MX25L3233FZBI-08G/Q	OTP region for unique serial number	OTP	No	No	4Kib	None
XC7A15T-1CPG236C	eFUSE Register	OTP	Yes, once	No	256 bits	None
XC7A15T-1CPG236C	eFUSE Register	OTP	Yes, once	No	32 bits	None
XC7A15T-1CPG236C	eFUSE Register	OTP	No	No	64 bits	None
XC7A15T-1CPG236C	eFUSE Register	OTP	Yes, once	No	14 bits	None