

## Nexys Video™ Statement of Volatility

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This document applies to the Nexys Video rev. A.X

This document lists the location, purpose, capacity, volatility and (re)programmability of memory devices that may be installed on the Nexys Video (SKU: 410-316). The terms programmable and erasable refer to normal means of access available to the public. It does not include reverse-engineering or any other attempts to extract data from these devices.

The content of this document is provided for information purposes only.

### Volatile memory

| IC  | Memory Purpose                                | User programmable | User removable | Size             | Reset procedure                |
|---|---|-------------------|----------------|------------------|--------------------------------|
| MT41K256M16HA-125<br>or<br>MT41K256M16HA-107<br>or<br>A3T4GF40ABF-GML | DDR3 Program /<br>Data memory                 | Yes               | No             | 4 Gib            | Remove power<br>for 60 seconds |
| XC7A200T-1SBG484  | Configuration<br>data for internal<br>latches | Yes               | No             | 77845216<br>bits | Remove power<br>for 60 seconds |
| XC7A200T-1SBG484  | Block RAM                                     | Yes               | No             | 13140 Kib        | Remove power<br>for 60 seconds |
| XC7A200T-1SBG484  | Distributed RAM                               | Yes               | No             | 2888 Kib         | Remove power<br>for 60 seconds |

## Non-volatile memory

| IC                   | Memory Purpose                      | Technology | User programmable | User removable | Size     | Reset procedure   |
|----------------------|-------------------------------------|------------|-------------------|----------------|----------|---|
| 93LC56BT-I/OT        | USB ID and Configuration            | EEPROM     | Yes               | No             | 2 KiB    | Electrically erasable from applications like FTDI's FTProG; Digilent's private application can restore factory functionality  |
| S25FL128SAGMFI00     | FPGA configuration and data storage | FLASH      | Yes               | No             | 128 Mib  | Electrically erasable; Use iMPACT or Vivado Software Tools available from Xilinx  |
| 24AA025E48           | Ethernet MAC Address                | EEPROM     | Yes/no            | No             | 2 KiB    | Lower half (00h-80h) can be programmed through I2C through the FPGA as per Microchip's 24AA025E48 datasheet, the upper half of memory is permanently write protected. |
| PIC24FJ128GB106-I/MR | USB embedded HID Host application   | FLASH      | Yes               | No             | 64 KiB   | User programmable through Microchip's MPLAB software with an external Microchip programmer  |
| FT232RQ-R            | USB ID and Configuration            | EEPROM     | Yes               | No             | 1024 bit | Electrically erasable from applications like FTDI's FTProG; Digilent's private application can restore factory functionality  |