

LGR-532x Statement of Volatility

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 This document applies to the LGR-532x series

This document lists the location, purpose, capacity, volatility and (re)programmability of memory devices that may be installed on the LGR-532x series devices (LGR-5325, LGR-5327, and LGR-5329). The terms programmable and erasable refer to normal means of access available to the public. It does not include reverse-engineering or any other attempts to extract data from these devices.

The content of this document is provided for information purposes only.

Volatile Memory

Memory Purpose	User programmable	User removable	Size	Reset procedure
Microcontroller RAM	No	No	64 KBytes	Remove power for 60 seconds
FPGA block / distributed RAM	No	No	344 Kbits	Remove power for 60 seconds
Data memory	No	No	16 Mbits	Remove power for 60 seconds

Non-Volatile Memory

Memory Purpose	Technology	User programmable	User removable	Size	Reset procedure
Microcontroller firmware	Flash	No	No	512 Kbytes	None required – not user modifiable
FPGA configuration, calibration data, logging configuration	Flash	Yes	No	512 KBytes	Electrically erasable; Use the control application to clear the scan list
Logging Data storage	SD Card	Yes	Yes	Variable	Remove from device and perform secure format with a PC